

FIG.1

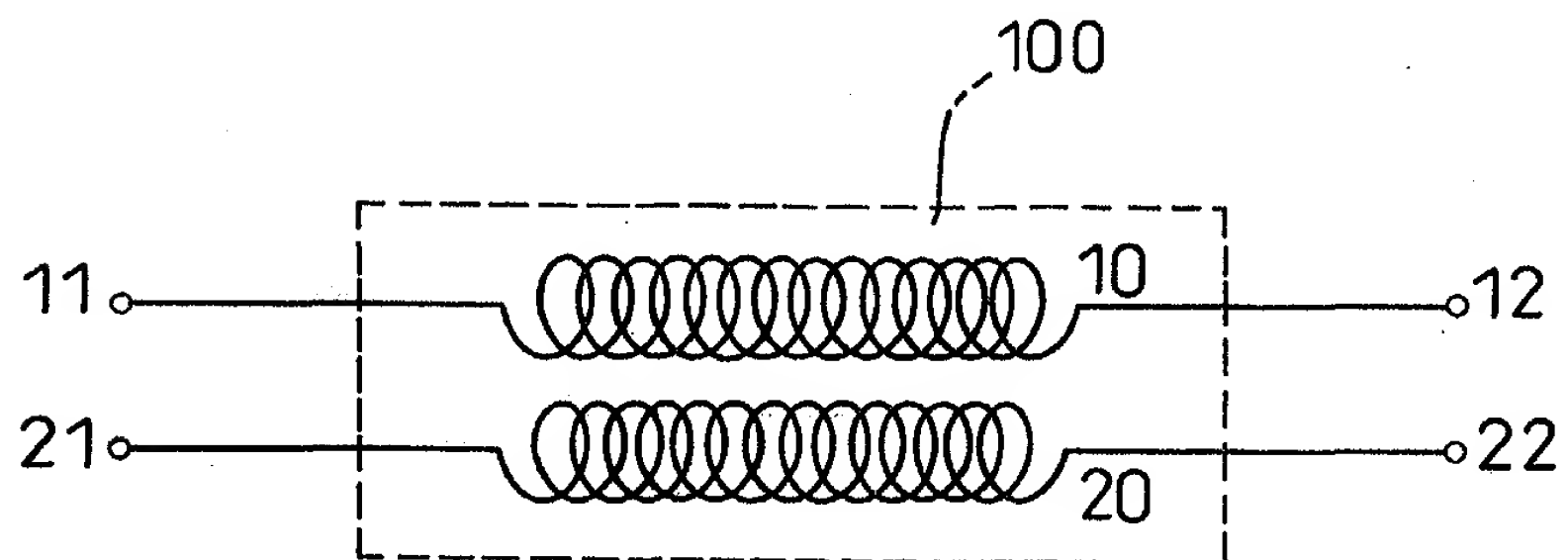


FIG. 2

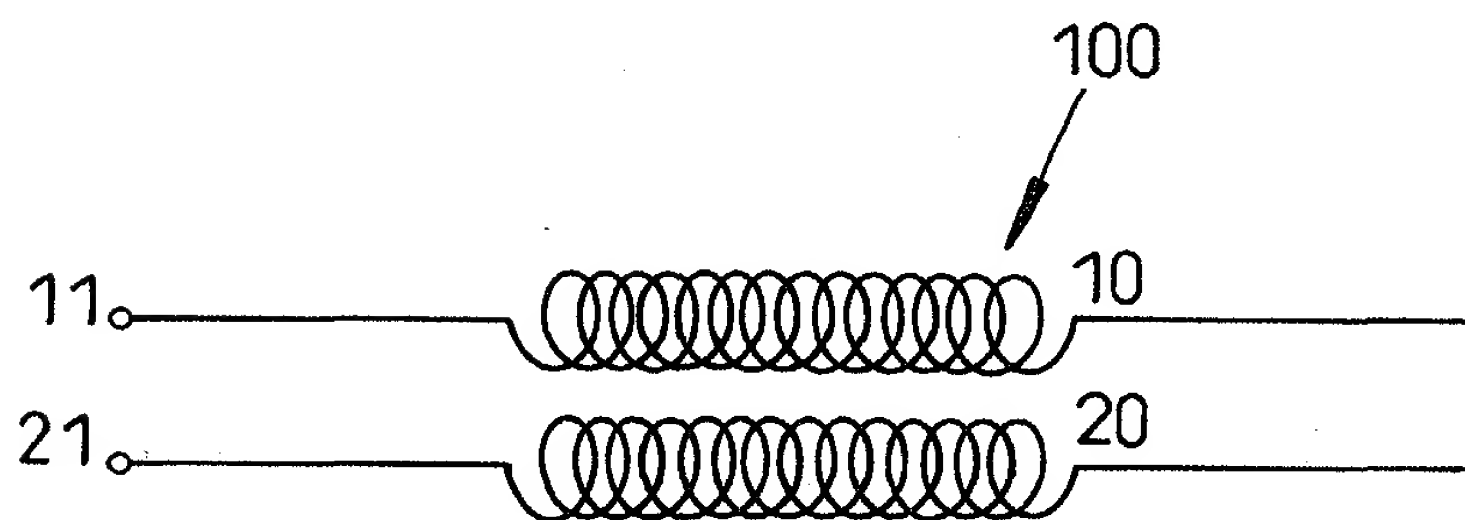


FIG. 3

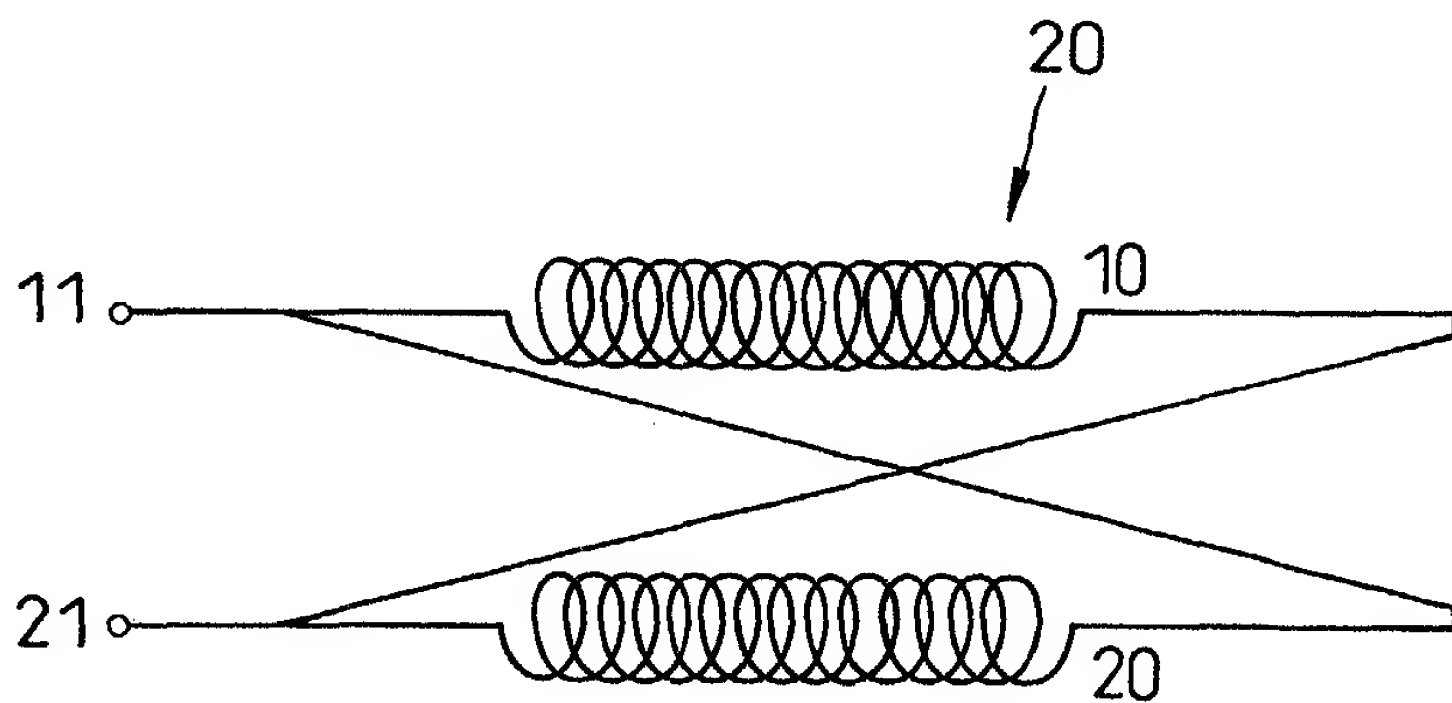


FIG.4

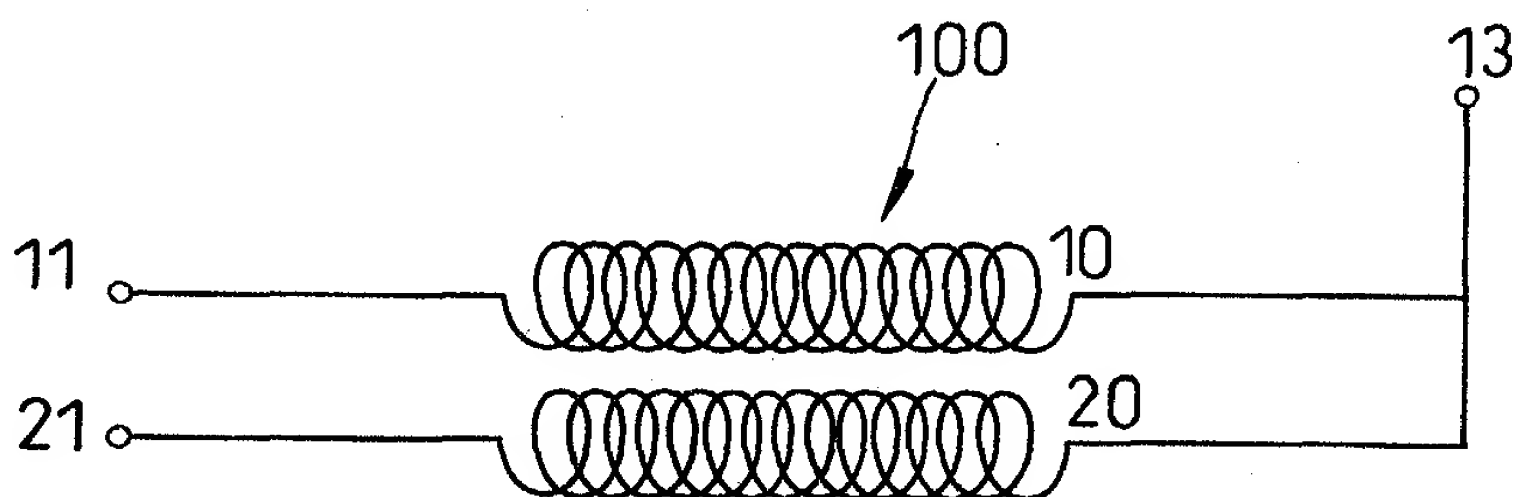


FIG.5

FIG. 4

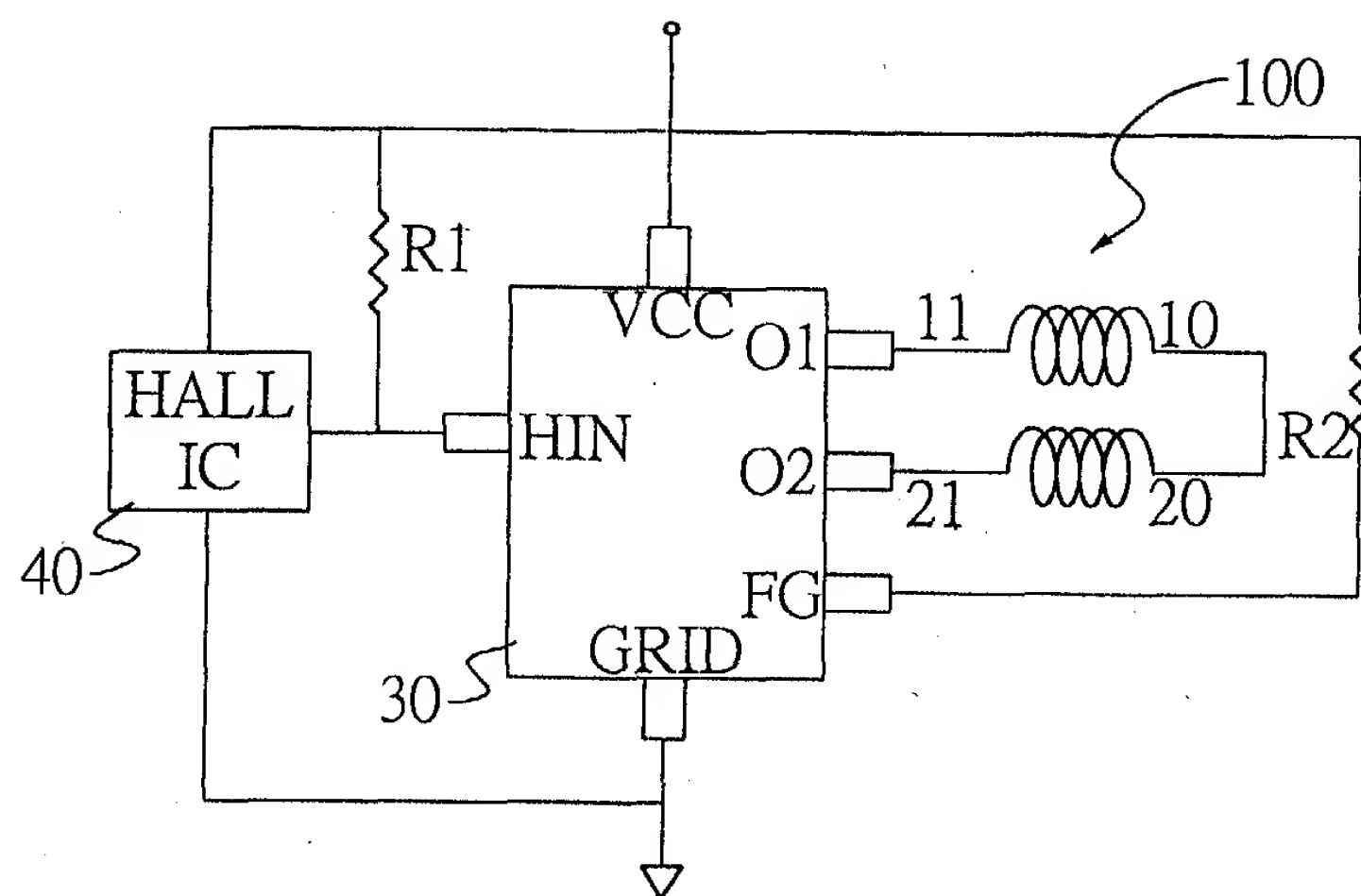
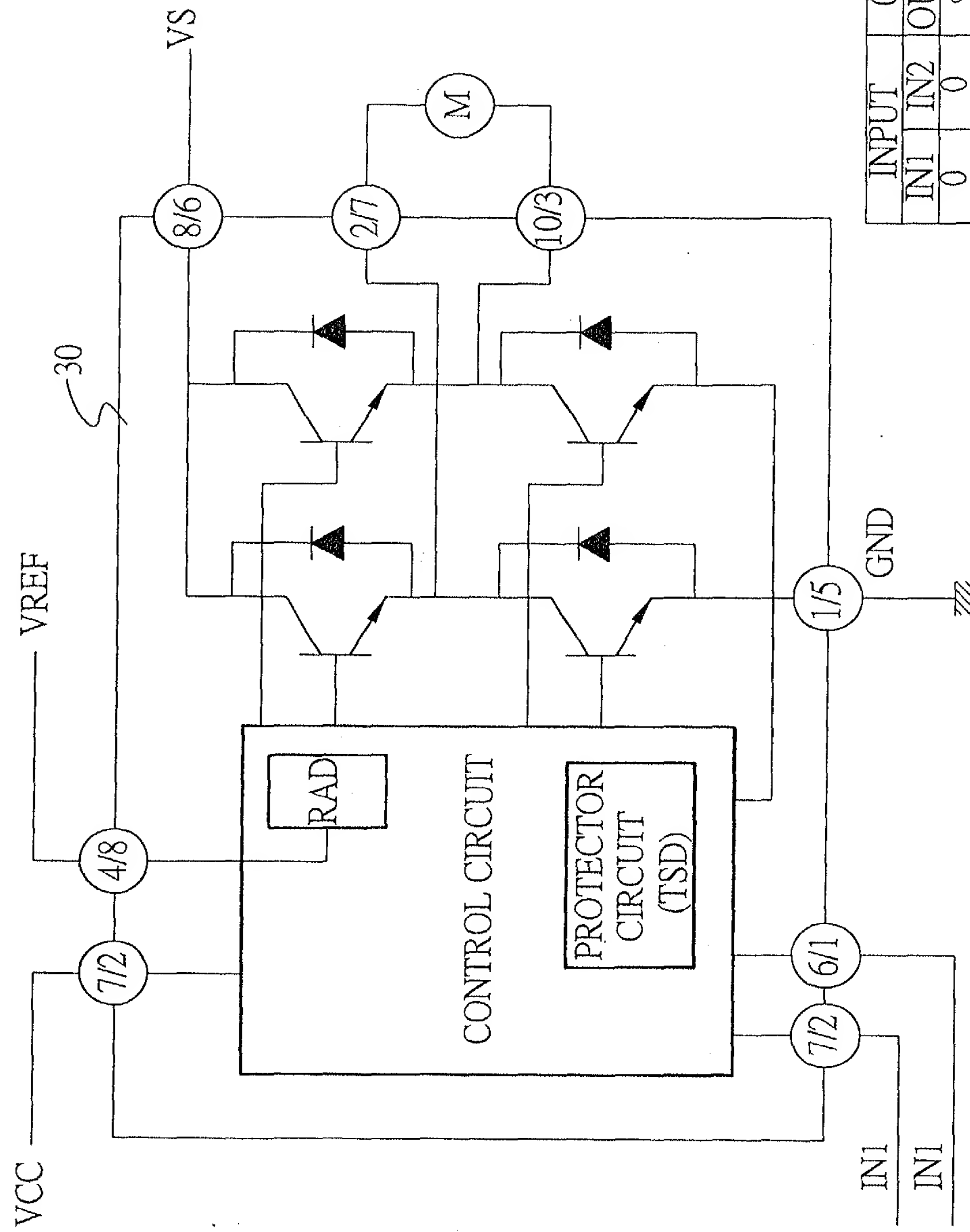


FIG. 6

[illegible]

INPUT		OUTPUT		MODE
IN1	IN2	OUT1	OUT2	
0	0	∞	∞	STOP
1	0	H	L	CW CCW
0	1	L	H	CCW CW
1	1	L	L	BRAKE

FIG. 7